

5-8 Saved

\*S1: (5991) (clock or pulse) near9 width near9 limit\$4  
 \*S2: (54936) (clock or pulse) near9 design\$4  
 \*S3: (1380905) (override or select\$4 or detect\$4)near9 signal  
 \*S4: (661) S1 and S2 and S3  
 \*S5: (63348) (inverter or gate)near9 delay  
 \*S6: (221) S4 and S5  
 \*S7: (467667) (clock or pulse) near9 generat\$4  
 \*S8: (212) S6 and S7  
 \*S9: (36) ((phase-locked-loop) or PLL)and S8  
 \*S10: [3] pulse same limit\$4 same multiplica\$4 same override\$4

0 Tagged (0)

Benutzername:   ☒ Hauptkategorie:

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pulse: same limit$4 same multiplex$6 same
overrid$3
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6704908

U	1	Document ID	Title	Current Current
1	U	US 6114933 A	Monitoring and controlling system for battery and battery charger	325/10 320/10
2	U	US 6087805 A	Monitoring and controlling system for battery and battery charger	320/10
3	U	US 5535337 A	Port circuit for a token ring concentrator having phase lock loop timing recovery with a	709/24 713/50